

FAST SWITCHING THYRISTOR

ATF820

Repetitive voltage up to **2000 V**
Mean on-state current **725 A**
Surge current **9 kA**
Turn-off time **50 μ s**

FINAL SPECIFICATION

gen 18 - ISSUE : 06

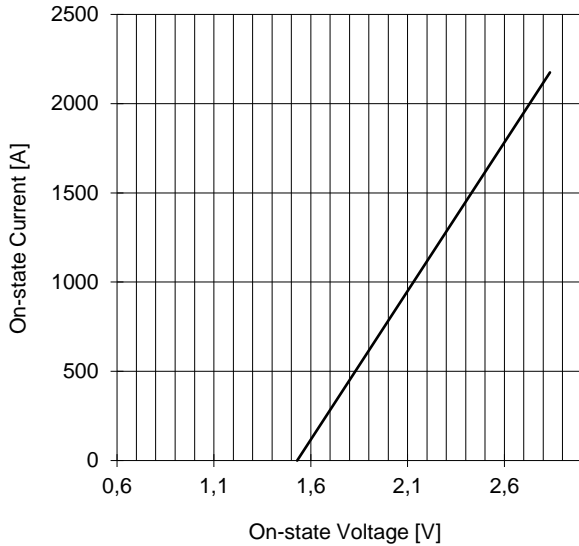
Symbol	Characteristic	Conditions	T _J [°C]	Value	Unit																								
BLOCKING																													
V _{RRM}	Repetitive peak reverse voltage		125	2000	V																								
V _{RSM}	Non-repetitive peak reverse voltage		125	2100	V																								
V _{DRM}	Repetitive peak off-state voltage		125	2000	V																								
I _{RRM}	Repetitive peak reverse current	V=V _{RRM}	125	100	mA																								
I _{DRM}	Repetitive peak off-state current	V=V _{DRM}	125	100	mA																								
CONDUCTING																													
I _{T(AV)}	Mean on-state current	180° sin, 50 Hz, Th=55°C, double side cooled		725	A																								
I _{T(AV)}	Mean on-state current	180° sin, 1 kHz, Th=55°C, double side cooled		645	A																								
I _{TSM}	Surge on-state current, non repetitive	sine wave, 10 ms	125	9	kA																								
I ² t	I ² t	without reverse voltage		405 x1E3	A ² s																								
V _T	On-state voltage	On-state current = 1400 A	25	2,4	V																								
V _{T(TO)}	Threshold voltage		125	1,53	V																								
r _T	On-state slope resistance		125	0,600	mohm																								
SWITCHING																													
di/dt	Critical rate of rise of on-state current, min	From 75% V _{DRM} up to 1200 A, gate 20V 10 ohm	125	400	A/ μ s																								
dv/dt	Critical rate of rise of off-state voltage, min	Linear ramp up to 70% of V _{DRM}	125	600	V/ μ s																								
td	Gate controlled delay time, typical	VD=100V, gate source 20V, 10 ohm, tr=1 μ s	25	0,6	μ s																								
tq	Circuit commutated turn-off time	di/dt = 20 A/ μ s, I = 400 A dv/dt = 200 V/ μ s, up to 75% V _{DRM}	125	50	μ s																								
Q _{rr}	Reverse recovery charge	di/dt = 60 A/ μ s, I = 1000 A	125	550	μ C																								
I _{rr}	Peak reverse recovery current	V _R = 50 V		205	A																								
I _H	Holding current, typical	VD=5V, gate open circuit	25	500	mA																								
I _L	Latching current, typical	VD=5V, tp=30 μ s	25	280	mA																								
GATE																													
V _{GT}	Gate trigger voltage	VD=5V	25	3,5	V																								
I _{GT}	Gate trigger current	VD=5V	25	350	mA																								
V _{GD}	Non-trigger gate voltage, min.	VD=V _{DRM}	125	0,25	V																								
V _{FGM}	Peak gate voltage (forward)		25	30	V																								
I _{FGM}	Peak gate current		25	10	A																								
V _{RGM}	Peak gate voltage (reverse)		25	5	V																								
P _{GM}	Peak gate power dissipation	Pulse width 100 μ s	25	150	W																								
P _{G(AV)}	Average gate power dissipation		25	3	W																								
MOUNTING																													
R _{th(j-h)}	Thermal impedance, DC	Junction to heatsink, double side cooled		37	°C/kW																								
T _j	Operating junction temperature			-30 / 125	°C																								
F	Mounting force			11.0 / 13.0	kN																								
	Mass			320	g																								
<p>ORDERING INFORMATION : ATF820 S 20 S — tq code</p> <p>standard specification — V_{DRM}&V_{RRM}/100</p> <table border="1" style="float: right;"> <thead> <tr> <th colspan="6">tq code</th> </tr> </thead> <tbody> <tr> <td>D 10 μs</td> <td>C 12 μs</td> <td>B 15 μs</td> <td>A 20 μs</td> <td>L 25 μs</td> <td></td> </tr> <tr> <td>M 30 μs</td> <td>N 35 μs</td> <td>P 40 μs</td> <td>R 45 μs</td> <td>S 50 μs</td> <td></td> </tr> <tr> <td>T 60 μs</td> <td>U 70 μs</td> <td>W 80 μs</td> <td>X 100μs</td> <td>Y 150μs</td> <td></td> </tr> </tbody> </table>						tq code						D 10 μ s	C 12 μ s	B 15 μ s	A 20 μ s	L 25 μ s		M 30 μ s	N 35 μ s	P 40 μ s	R 45 μ s	S 50 μ s		T 60 μ s	U 70 μ s	W 80 μ s	X 100 μ s	Y 150 μ s	
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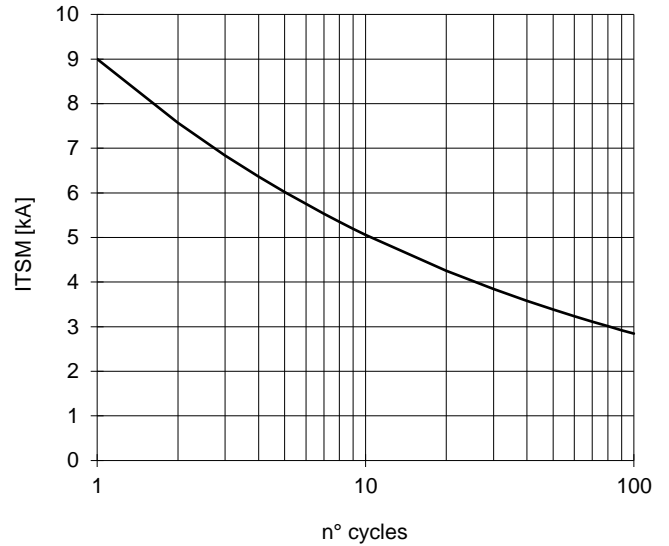


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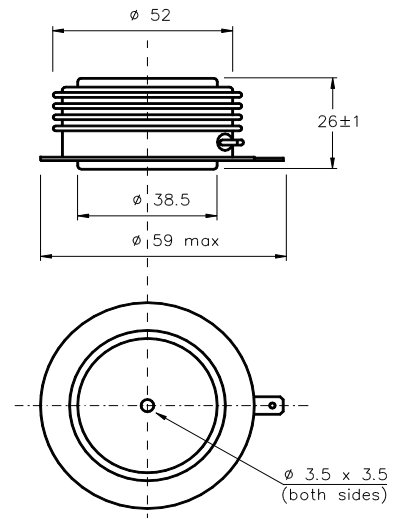
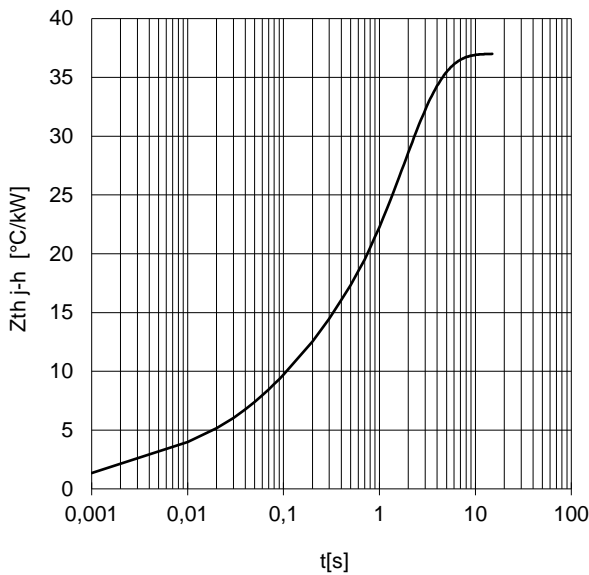
ON-STATE CHARACTERISTIC
T_j = 125 °C



SURGE CHARACTERISTIC
T_j = 125 °C



TRANSIENT THERMAL IMPEDANCE
DOUBLE SIDE COOLED



Dimensions
in mm



Cathode terminal type DIN 46244 - A 4.8 - 0.8

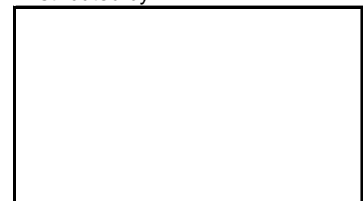
Gate terminal type AMP 60598 - 1

All the characteristics given in this data sheet are guaranteed only with uniform clamping force, cleaned and lubricated heatsink, surfaces with flatness < .03 mm and roughness < 2 μm.

In the interest of product improvement POSEICO SpA reserves the right to change any data given in this data sheet at any time without previous notice.

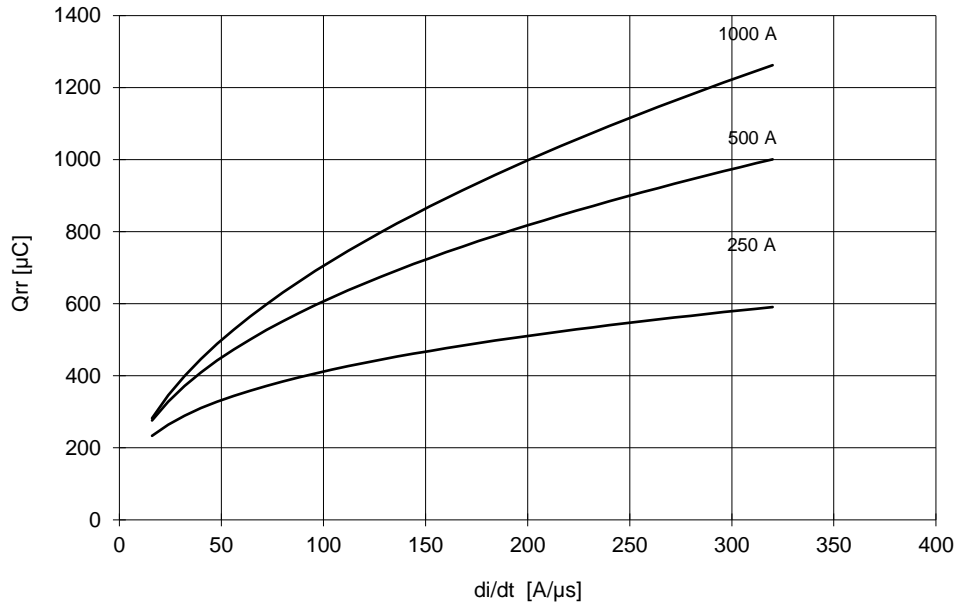
If not stated otherwise the maximum value of ratings (symbols over shaded background) and characteristics is reported.

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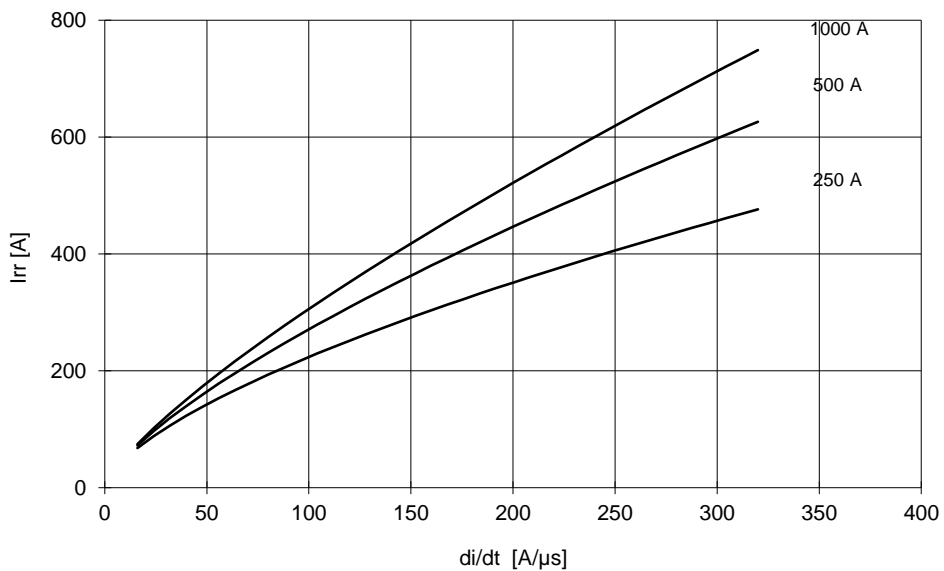


SWITCHING CHARACTERISTICS

REVERSE RECOVERY CHARGE
T_j = 125 °C



REVERSE RECOVERY CURRENT
T_j = 125 °C



$$t_a = I_{rr} / (di/dt) \quad t_b = t_{rr} - t_a$$

$$\text{Softness (s factor)} \quad s = t_b / t_a$$

$$\text{Energy dissipation during recovery } E_r = V_r \cdot (Q_{rr} - I_{rr} \cdot t_a / 2)$$

